devrintalen

objective

To obtain a full time computer engineering position in which I can apply my skills in architecture, validation, and embedded system design.

education

- 2004-2008 **B.S.** Electrical and Computer Engineering Cornell University, Ithaca, NY Cum Laude, Dean's List, GPA: 3.4
- 2000-2004 High School Walt Whitman, Bethesda, MD Honor Roll, National Merit Scholar

experience

- 2008- **System Validation Engineer, Intel Corporation** Hudson, MA Testing mission-critical server processors for bugs. Experience working in a lab with Tektronix logic analyzers, developing test plans, test content, and debugging system failures. Worked through multiple projects from first silicon to first sale in a variety of roles including triage and debug, RTL development, FPGA emulation, and software developer.
- 2007 **Embedded System Developer, Cornell University Satellite Team** Programmed the interface boards connecting several modules to a backplane. Developed an OS driver for debugging the AVR microcontroller-based system. Team competed in the University Nanosat-4 Program.
- 2006–2007 Co-op, Intel Corporation Hudson, MA Developed a tool to analyze the fault coverage contribution of sROSL sites on an Itanium processor design. Data generated by this tool impacted design decisions about which sROSL sites could be removed.
 2005 Intern, Naval Research Labs Washington, DC

Performed research experiments with in a Class 4 terawatt laser facility. Responsibilities included setting up equipment, tuning optics, and carrying out experiments. Passed security clearance requirements.

projects

2012 **ADBUSB**

USB adapter for the 1987 Apple Extended Keyboard. Translates Apple Desktop Bus (ADB) protocol into HID-compliant USB. Developed the entire project, from the microcontroller code to the schematics to the PCB layout.

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about

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languages

english turkish

programming

C, Assembly (x86, IA64, AVR) Verilog Python, Perl Javascript (ES5, node.js) CSS3 & HTML5